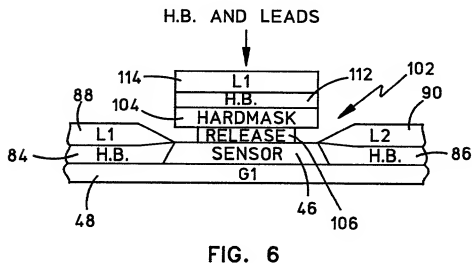
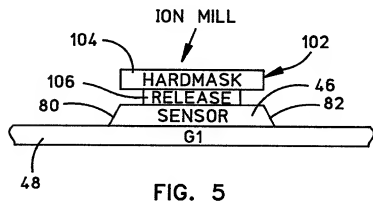
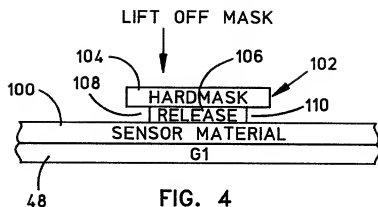


FIG. 2





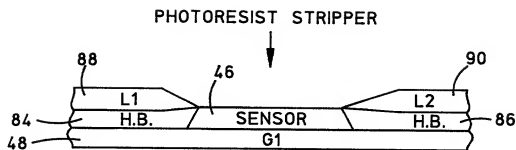


FIG. 7

DEPOSIT RELEASE, HARDMASK AND RESIST LAYERS

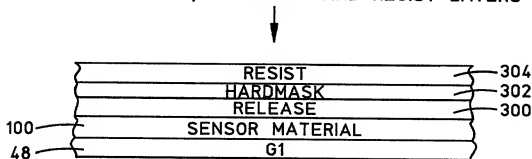


FIG. 8

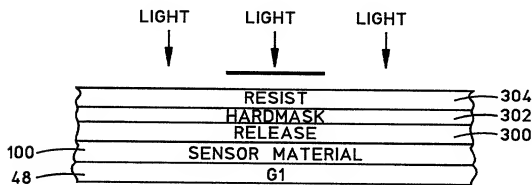


FIG. 9



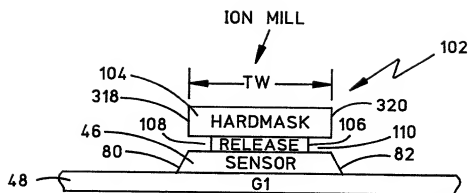


FIG. 14

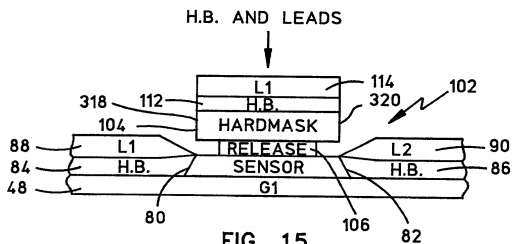


FIG. 15

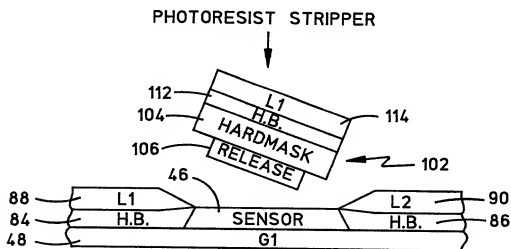


FIG. 16